

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
26860/33:43DAPPLICATION NO.
08/959,140INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)APPLICANT
Yunlong Sun and Edward J. SwensonFILING DATE
October 28, 1997GROUP
1742

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
GSE	AA	5 6 2 3 4 4 9	04/22/97	Fischer et al.	315	200	08/11/95
GSE	AB	5 1 8 5 2 9 1	02/09/93	Fischer et al.	437	173	09/06/91
GSE	AC	5 0 6 6 9 9 8	11/19/91	Fischer et al.	357	51	
GSE	AD	4 8 5 3 7 5 8	08/01/91	Fischer	357	51	
GSE	AE	4 8 1 0 0 4 9	03/07/89	Fischer et al.	350	96.12	
GSE	AF	4 5 9 8 0 3 9	07/01/86	Fischer et al.	430	323	
GSE	AG	4 2 2 8 5 2 8	10/14/80	Cenker et al.	365	200	
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AI							
	AJ							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

GSE	AK	1988 "Laser Programming of Gallium Arsenide HFET Digital Integrated Circuits," Fischer
GSE	AL	1981 "Laser Programmable Redundancy and Yield Improvement in a 64K DRAM," Smith et al
GSE	AM	1980 "Trimming Behavior and Post-Trim Characteristics of Ta ₂ N resistors on Silicon," Kestenbaum and Baer
GSE	AN	1979 "Interference Effects in Laser Micromachining of Thin Films on Silicon," Kestenbaum
GSE	AO	1979 "A Fault-Tolerant 64K Dynamic Random-Access Memory," Cenker et al.
GSE	AP	1976 "Laser Coding of Bipolar Read-Only Memories," North and Weick

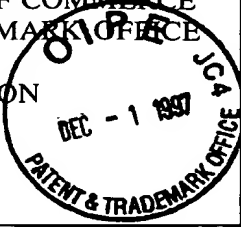
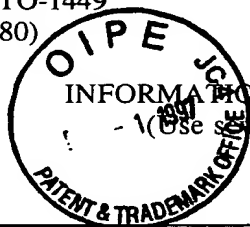
EXAMINER

Geoffrey Evans

DATE CONSIDERED

April 13, 1998

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
26860/33:43 DAPPLICATION NO.
08/959,140APPLICANT
Yunlong Sun and Edward J. SwensonFILING DATE
October 28, 1997GROUP
1742

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

GSE	AA ✓	1981 "A Technique for Automatic Focusing and Alignment of Monolithic Circuits for Laser Trimming," Wells et al.
GSE	AB ✓	1980 "Photoexcitation Effects During Laser Trimming of Thin-Film Resistors on Si," Kestenbaum & Baer
GSE	AC ✓	1980 "Lasers in Semiconductor Processing," Burggraaf
GSE	AD ✓	1979 "Reliability of LSI Memory Circuits Exposed to Laser Cutting," Rand
GSE	AE ✓	1979 "Interference Effects in Laser Micromachining of Thin Films on Silicon," Kestenbaum
GSE	AF ✓	1979 "A Fault-Tolerant 64k Dynamic Ram," Cenker, et al.
GSE	AG ✓	1978 "Effect of Vapor Plasma on the Coupling of Laser Radiation with Al Targets," Shui, et al.
GSE	AH ✓	1978 "A Passive Laser-Trimming Technique to Improve the Linearity of a 10-bit D/A Converter," Price
GSE	AI ✓	1977 "Laser Vaporization of Metal Films - Effect of Optical Interference in Underlying Dielectric Layers," North
GSE	AJ ✓	1976 "A Passive Laser-Trimming Technique to Improve D/A Linearity," Price
GSE	AK ✓	1975 "Connections and Disconnections on Integrated Circuits Using Nanosecond Laser Pulses," Cook, et al.
GSE	AL ✓	1974 "An Overview of Laser Functional Trimming Techniques," Hurtig & Swenson (ESI)
GSE	AM	1974 - "Algorithmic Trimming on Active Circuitry," Faber
GSE	AN	1965 - Graph of Transmission & Absorption of Crystals & Glasses

EXAMINER

Geoffrey S Evans

DATE CONSIDERED

04/13/98

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
26860/33:43 DAPPLICATION NO.
08/959,140INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)APPLICANT
Yunlong Sun and Edward J. SwensonFILING DATE
October 28, 1997GROUP
17/2

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
GSE	AA	5 0 9 1 9 9 1	02/25/92	Tulip et al.	372	4	
GSE	AB	4 9 6 5 8 0 3	10/23/90	Esterowitz et al.	372	5	
GSE	AC	4 9 4 2 5 8 2	07/17/90	Kintz et al.	372	18	
GSE	AD	4 9 3 0 9 0 1	06/05/90	Johnson et al.	372	26	
GSE	AE	4 7 3 1 7 9 5	03/15/88	Clark et al.	372	107	
GSE	AF	4 6 5 6 6 3 5	04/07/87	Baer et al.	372	27	
GSE	AG	4 1 7 2 7 4 1	10/30/79	Johnson	148	1.5	
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AJ							
	AK							
	AL							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

GSE	AM	1995 "Optimization of Memory Redundancy Laser Link Processing," Sun et al.
GSE	AN	1995 "New Laser Technology Addresses the Yield Enhancement Challenge of the Evolving Generation of Memory Devices," Hutchens
GSE	AO	1993 "Application of a Binary Diffractive Optic for Beam Shaping in Semiconductor Processing by Lasers," Cordingley
GSE	AP	1991 "Focused Ion Beams," Orloff
GSE	AQ	1990 "Polysilicon Resistor Trimming by Laser Link Making," Parker, et al.

EXAMINER

Geoffrey S. Evans

DATE CONSIDERED

12/04/13/98

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
26860/33:43 DAPPLICATION NO.
08/959,140INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)APPLICANT
Yunlong Sun and Edward J. SwensonFILING DATE
October 28, 1997

GROUP

1742

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

GSE	AA /	1989 "Reliability Aspects of Laser Programmable Redundancy: Infrared v. Green, Polysilicon v. Silicide," Chlipala & Scarfone
GSE	AB	1989 "Polycrystalline Silicon Resistor Trimming by Laser Annealing," Crowley
GSE	AC /	1989 "Explosion of Poly-Silicide Links in Laser Programmable Redundancy for VLSI Memory Repair," Lu, Chlipala, Scarfone
GSE	AD	1989 "Computer-Simulated Explosion of Poly-Silicide Links in Laser-Programmable Redundancy for VLSI Memory Repair," Chlipala, Scarfone
GSE	AE /	1987 "Laser Processing for Application Specific Integrated Circuits (ASICs)," Smart & Stewart
GSE	AF	1986 - Graphs of Absortance of Al, Pt, Ni, W v. Wavelength
GSE	AG /	1986 "Computer Simulation of Target Link Explosion in Laser Programmable Redundancy for Si Memory," Scarfone & Chlipala
GSE	AH /	1984 "Considerations of Laser Material Interactions for the Trimming of Thin Film Resistors," Smart
GSE	AI /	1983 "Laser Adjustment of Linear Monolithic Circuits," Litwin & Smart
GSE	AJ /	1982 "Semiconductor Processing Applications with Lasers," Kestenbaum
GSE	AK /	1982 "Laser-Assisted Poly-Si Resistor Fuse Utilizing Silicon Lateral Flow," Iwai, et al.
GSE	AL /	1981 "Laser Programmable Redundancy and Yield Improvement in a 64k DRAM," Smith et al.
GSE	AM /	1981 "Laser Beam Processing," Cable

EXAMINER

Geoffrey S. Evans

DATE CONSIDERED

04/13/98

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.